

LITE-64

作者：MCU Ecosystem Development Team

改动记录

- 1. 2022/10/27 V1B 初版


使用说明

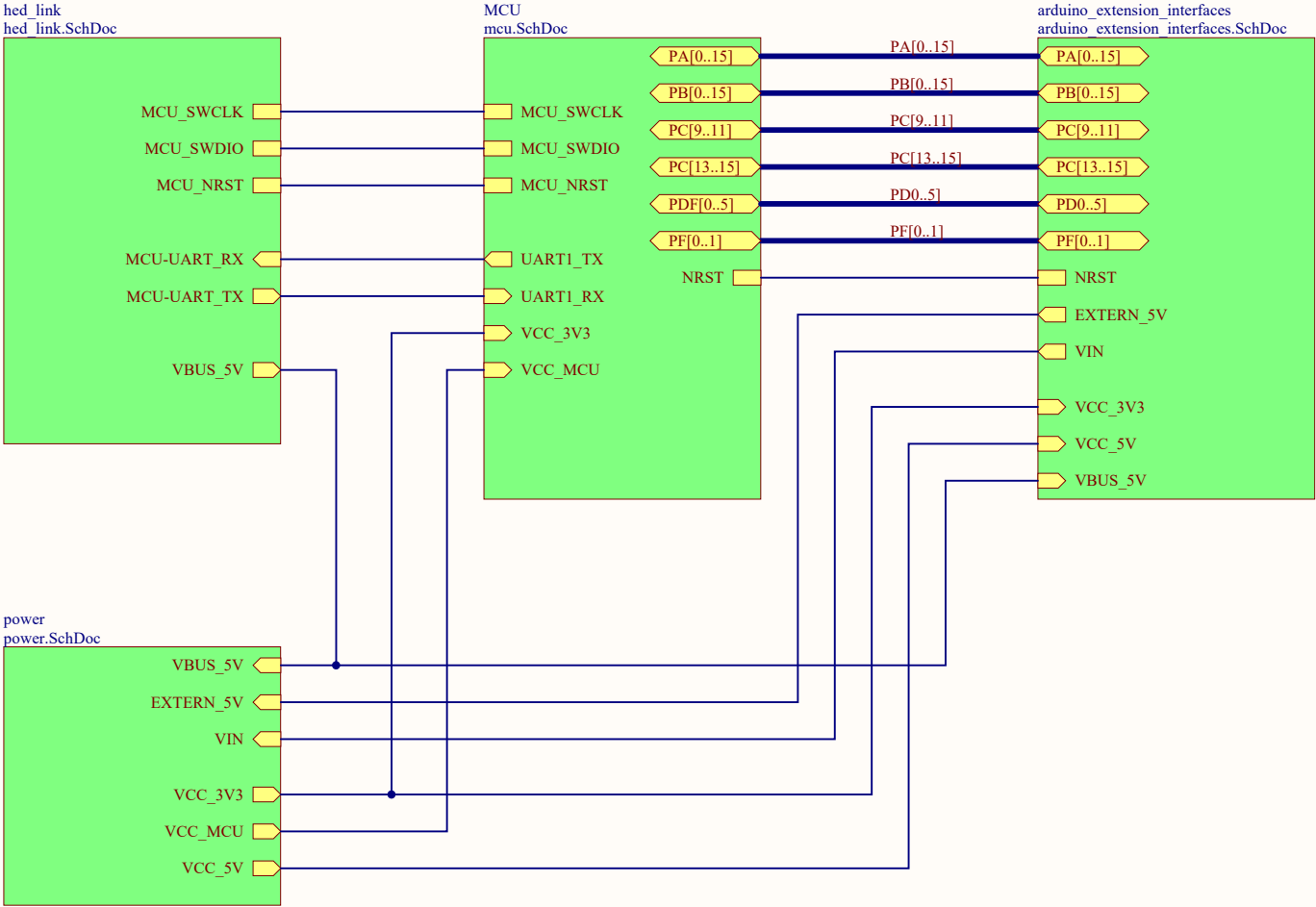
- USB接口用于程序下载与调试
- 1. USB接口作为HED_LINK:可用于程序下载与调试
 - 2. USB接口作为虚拟串口只可用于程序下
- 下载前将BOOT引脚接高电平，按复位按键


TOP
hb1003b_top.SchDoc

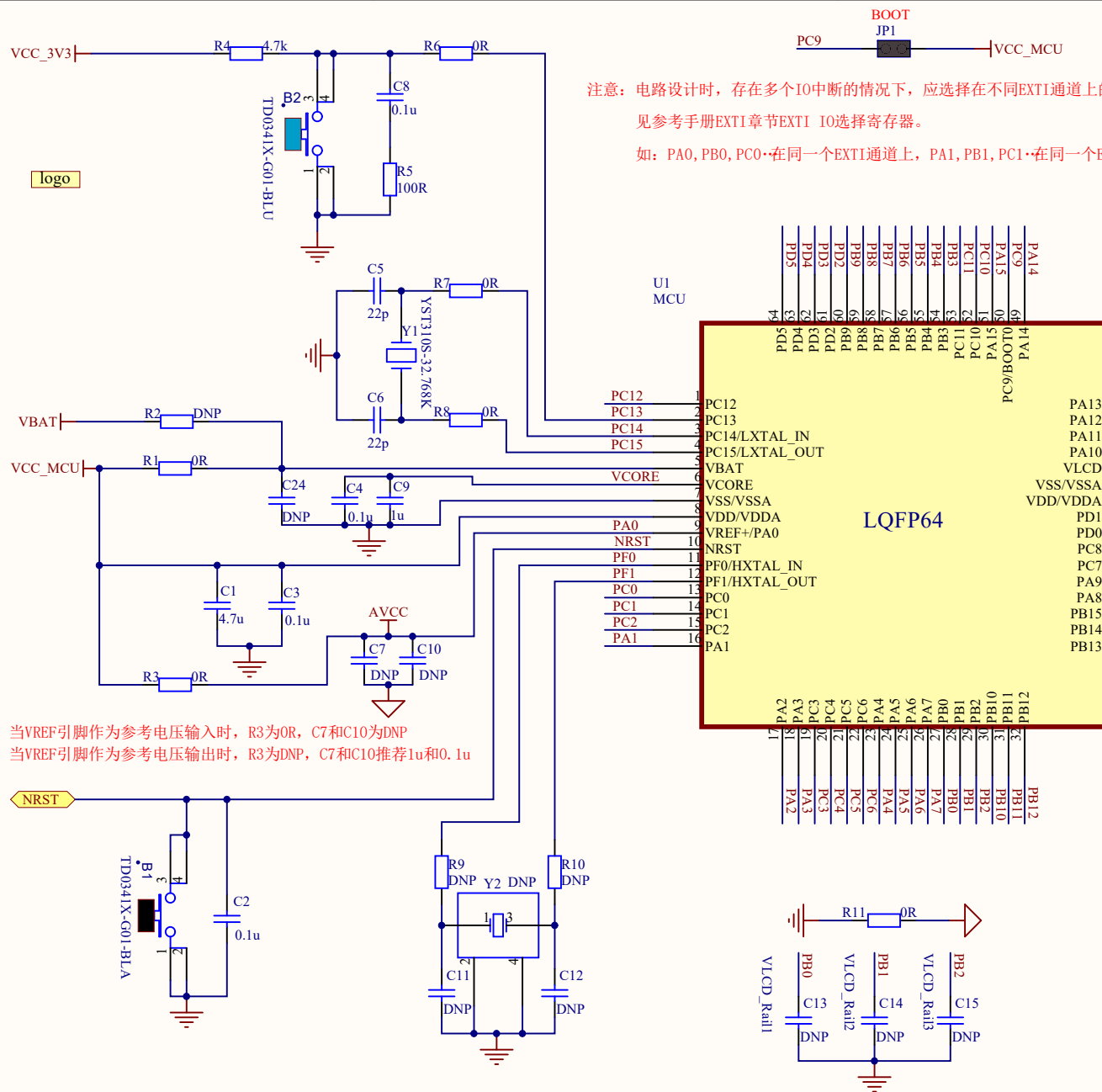


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Title: <u>project overview</u>		
Project: <u>LITE-64</u>		
Reference: <u>HB1003</u>	Rev: <u>B</u>	
Variant: <u>None</u>	Date: <u>2022-10-27</u>	
Size: <u>A4</u>	Sheet: <u>1</u> of <u>6</u>	



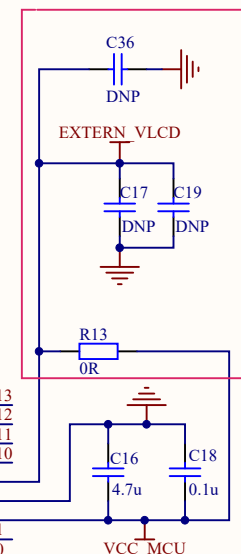
Title: top		
Project: LITE-64		
Reference: HB1003	Rev: B	
Variant: None	Date:2022-10-27	
Size: A4	Sheet: 2 of 6	



注意：电路设计时，存在多个IO中断的情况下，应选择在不同EXTI通道上的IO。
见参考手册EXTI章节EXTI IO选择寄存器。
如：PA0, PB0, PC0·在同一个EXTI通道上，PA1, PB1, PC1·在同一个EXTI通道上。

当VREF引脚作为参考电压输入时，R3为0R，C7和C10为DNP
当VREF引脚作为参考电压输出时，R3为DNP，C7和C10推荐1u和0.1u

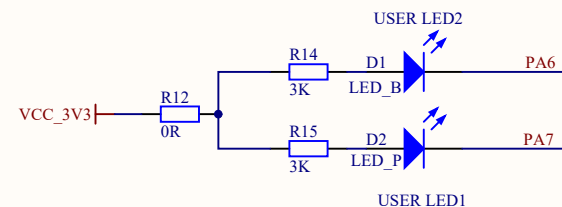
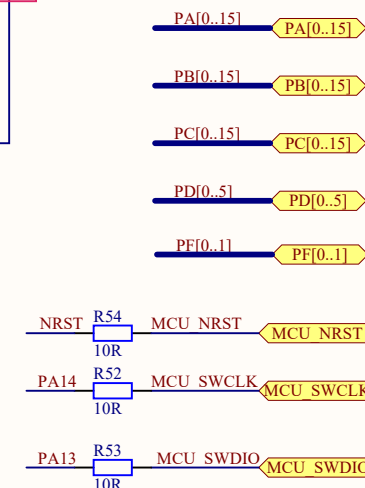
LCD控制器电源轨去耦，根据LCD屏选择去耦电容
推荐设置为LCD屏电容值的10倍




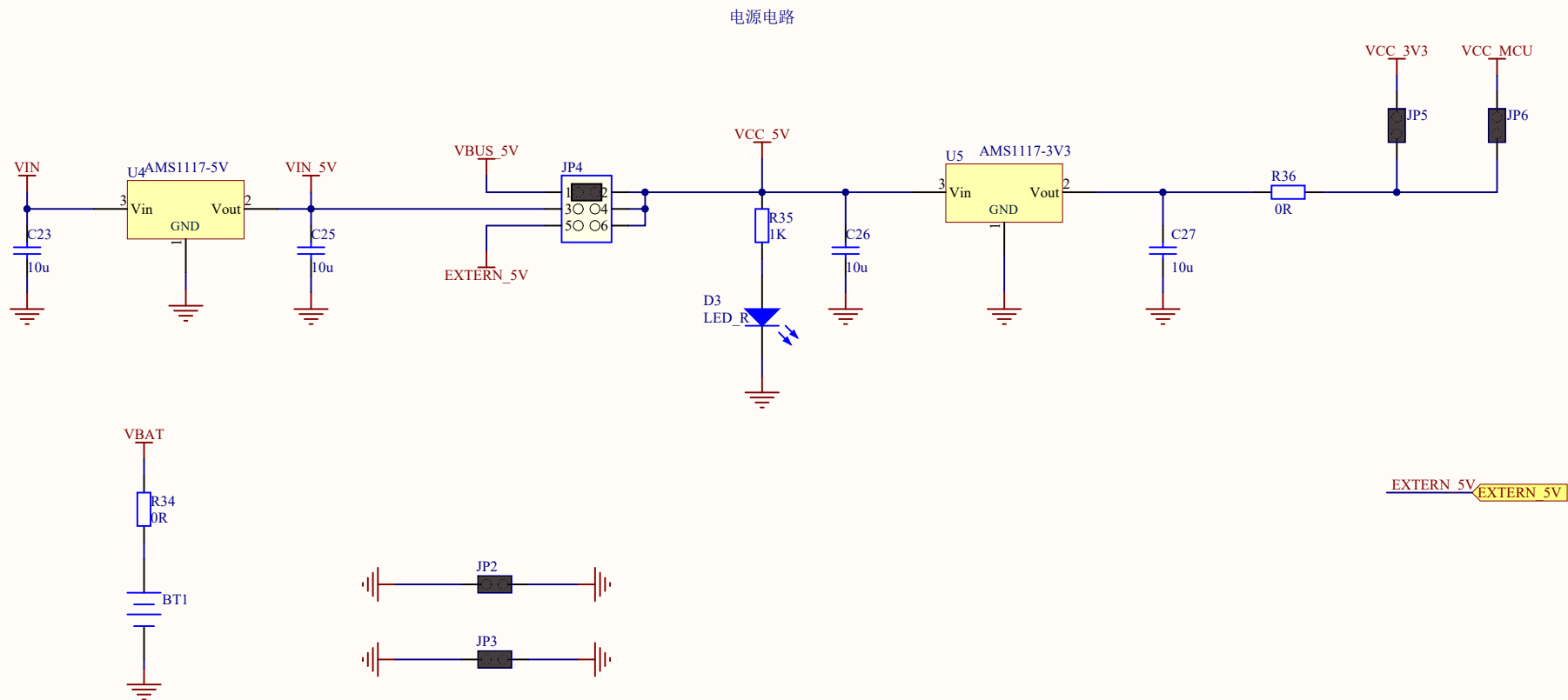
LCD控制器供电方式


- 1、采用内部电荷泵供电时，C36荐0.1uF
R13、C17、C19为DNP。
- 2、外部供电：VLCD = VCC_MCU
R13为0Ω，C36、C17、C19为DNP。
- 3、外部供电：VLCD ≠ VCC_MCU
C17、C19为1uF+0.1uF，C36、R13为DNP。

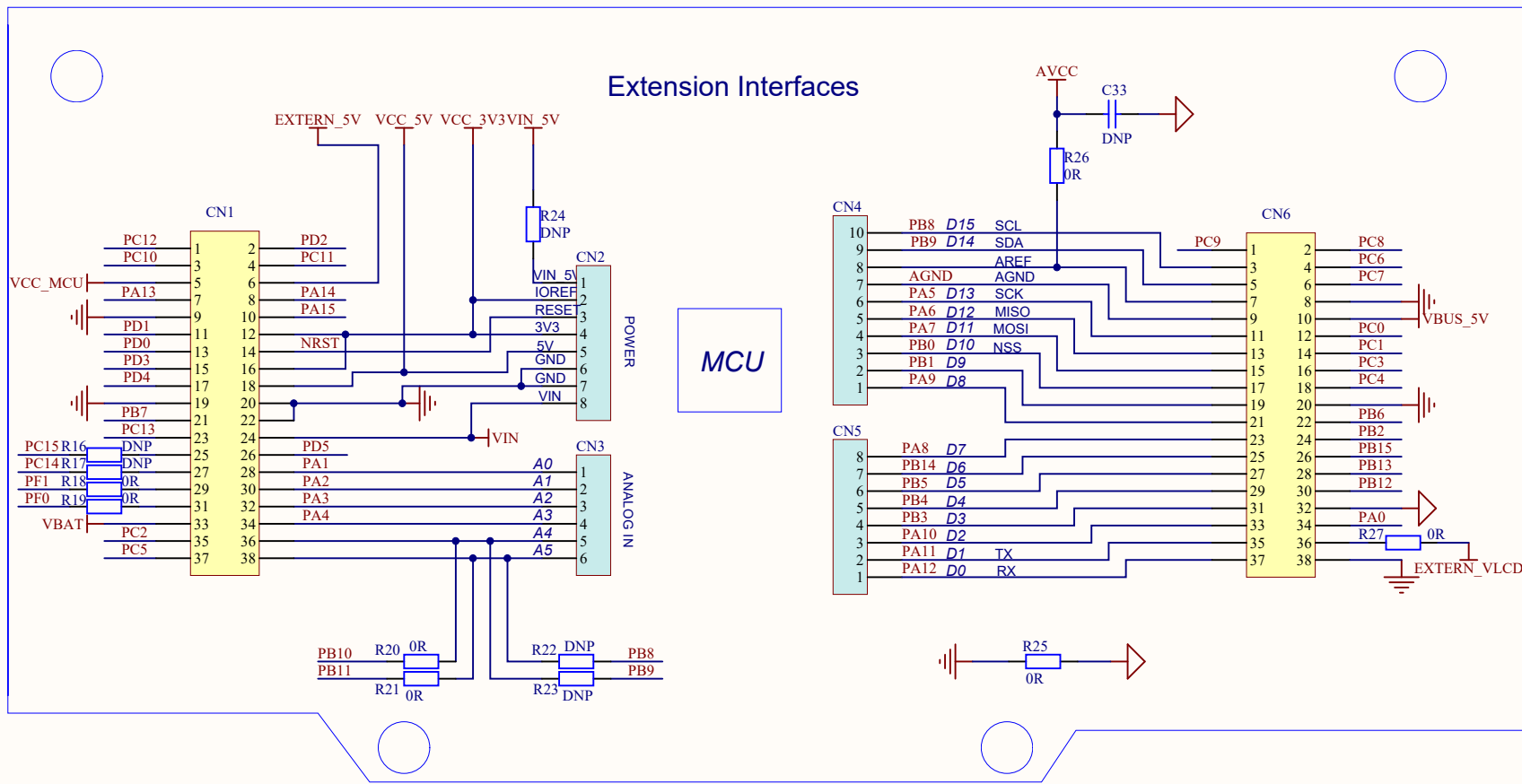
注意：无LCD外设的产品，R13/C17/C19/C36均为DNP。




Title:	MCU	
Project:	LITE-64	
Reference:	HB1003	
Variant:	None	
Size:	A4	Rev: B
		Date:2022-10-27
		Sheet: 3 of 6



Title: power		
Project: LITE-64		
Reference: HB1003	Rev: B	
Variant: None	Date:2022-10-27	
Size: A4	Sheet: 4 of 6	



- PA[0..15] → PA[0..15]
- PB[0..15] → PB[0..15]
- PC[0..15] → PC[0..15]
- PD[0..5] → PD[0..5]
- PF[0..1] → PF[0..1]
- NRST → NRST
- EXTERN_5V → EXTERN_5V

Title: arduino interface		
Project: LITE-64		
Reference: HB1003	Rev: B	
Variant: None	Date:2022-10-27	
Size: A4	Sheet: 5 of 6	

